

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5158	accelerated adj graphics adj port	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:49
L2	8464	chip adj set	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:49
L3	15289	chipset or 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:49
L4	1137	1 and 3	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:50
L5	805	address\$3 and 4	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:51
L6	101	order\$3 near3 x86	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:51
L7	3	5 and 6	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:52
L8	29	endian and 5	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:59
L9	10196	graphic\$3 near3 controller\$1	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:59
L10	158	endian and 9	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 15:59
L11	4	endian with 9	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 16:12
L12	332	712/300.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 16:12
L13	46054	token\$1	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 16:12
L14	15996	address near3 rang\$3	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 16:13
L15	855	order\$3 with 14	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 16:13

## EAST Search History

L16	15	12 and 15	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/04 16:13
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